

```

mov     ecx,dword ptr [EPC]
mov     byte ptr [ecx+7],al      // bits are already aligned
mov     eax,dword ptr [serialNumber]
movzx   eax,byte ptr [eax]
mov     ecx,dword ptr [EPC]
movzx   ecx,byte ptr [ecx+7]
or      ecx,eax
mov     eax,dword ptr [EPC]
mov     byte ptr [eax+7],cl      // bits are already aligned
mov     eax,dword ptr [EPC]
mov     ecx,dword ptr [serialNumber]
mov     cl,byte ptr [ecx+1]
mov     byte ptr [eax+8],cl      // bits are already aligned
mov     eax,dword ptr [EPC]
mov     ecx,dword ptr [serialNumber]
mov     cl,byte ptr [ecx+2]
mov     byte ptr [eax+9],cl      // bits are already aligned
mov     eax,dword ptr [EPC]
mov     ecx,dword ptr [serialNumber]
mov     cl,byte ptr [ecx+3]
mov     byte ptr [eax+0Ah],cl    // bits are already aligned
mov     eax,dword ptr [EPC]
mov     ecx,dword ptr [serialNumber]
mov     cl,byte ptr [ecx+4]
mov     byte ptr [eax+0Bh],cl
pop     edi
pop     esi
pop     ebx
add     esp,0D8h
cmp     ebp,esp
call    @ILT+925(__RTC_CheckEsp) (4113A2h)
leave
ret

```

Claims: Please amend claims 1-7 as shown below:

1. (amended) A method for converting or translating descriptions of products encoded in the Universal Product Code (UPC) or Uniform Code Council 12 (UCC-12) format to the Electronic Product Code (EPC) format comprising:

- a means of loading and storing registers;
- and a means of multiplying, dividing, shifting or adding register contents.

2. (amended) A method according to claim 1 wherein the operation of storage of a register involves only shift operations.

3. (amended) A method according to claim 1 wherein the operation of storage of a register involves only multiplication or division operations.